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Applicants: Vinodh R. Cuppa, et al.

Examiner: Shane F. Gerstl

Serial No.: 09/855,241

Art Unit: 2183

Filed: May 15, 2001

Docket: YOR920000809US1 (14138)

For: A METHOD AND APPARATUS FOR  
REDUCING LOGIC ACTIVITY IN A  
MICROPROCESSOR

Confirmation No.: 8988

Dated: January 26, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**AMENDMENT UNDER 37 CFR 1.111**

Sir:

Responsive to the Office Action of October 26, 2004, Applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendments to the Abstract are reflected on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 8 of this paper.

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: January 26, 2005  
Steven Fischman

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